

: ;

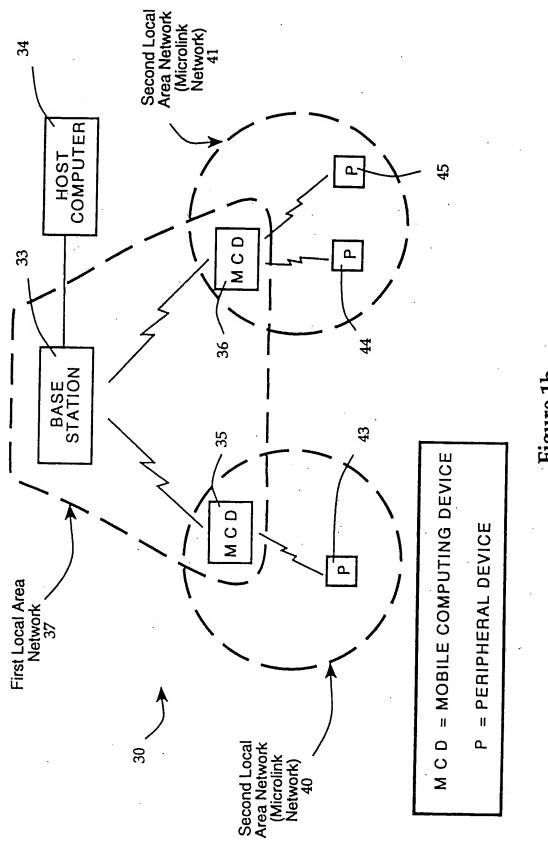


Figure 1b

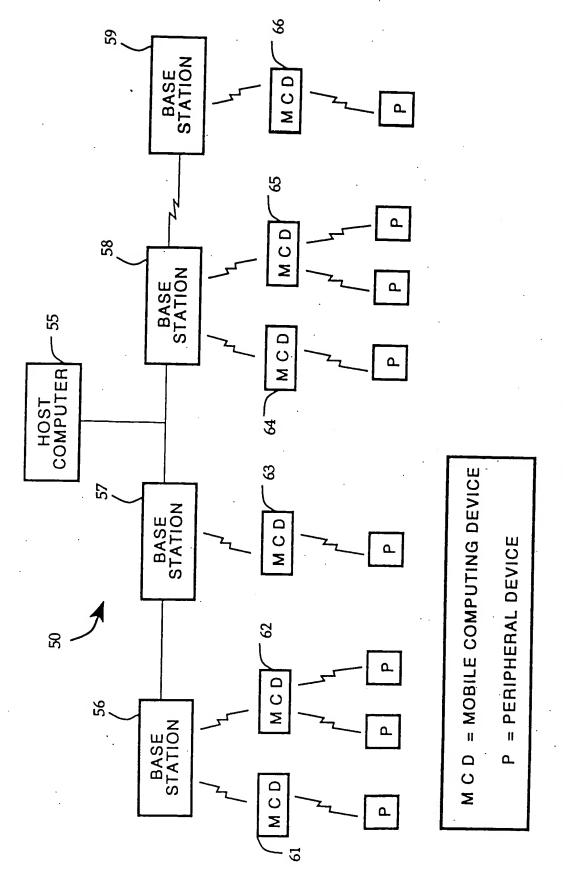


Figure 1c

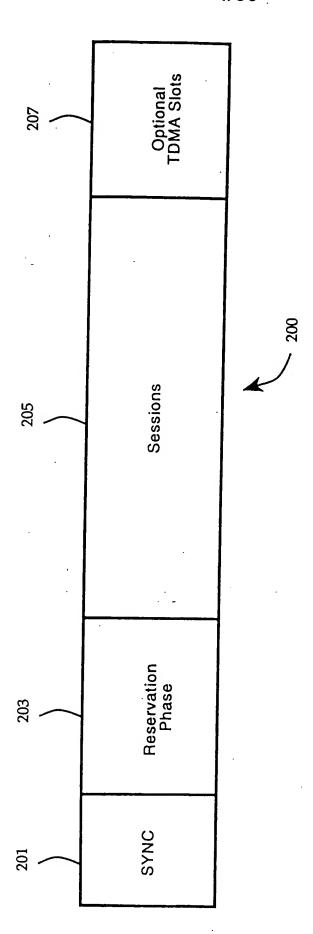


Figure 2

			7				_	
	10 ms	Freq. 1 2466 MHz	301		20 ms	Freq. 1 2466 MHz	303	
	10 ms	Freq. 79 2403 MHz	301		20 ms	Freq. 79 2403 MHz	303	
	10 ms				20 ms			
	10 ms	Freq. 5 2444 MHz	301	e 3a	20 ms	Freq. 5 2444 MHz	303	3b
·	10 ms	Freq. 4 2472 MHz	301	Figure 3a	20 ms	Freq. 4 2472 MHz	303	Figure 3b
	10 ms	Freq. 3 2417 MHz	301		20 ms	Freq. 3 2417 MHz	303	
	10 ms	Freq. 2 2411 MHz	301	·	20 ms	Freq. 2 2411 MHz	303	
	10 ms	Freq. 1 2466 MHz	301		20 ms	Freq. 1 2466 MHz	303	
				-				

	Freq 1: 2466 MHz	Freq 2: 2	Freq 2: 2411 MHz	Freq 3: ;	Freq 3: 2417 MHz	Freq 5:	Freq 5: 2444 MHz
20 A	AI. 2 20 ms	AI. 1 20 ms	AI. 2 20 ms	AI. 1 20 ms	AI. 2 20 ms	Al. 1	AI. 2 20 ms
			Figure 4a	re 4a			
Freq	Freq 1: 2466 MHz	6 MHz					
L					Freq 2:	Freq 2: 2411 MHz	
A 20	Al. 2 20 ms	Al. 3 20 ms	Al. 4 20 ms	Al. 1 20 ms	AI. 2 20 ms	AI. 3 20 ms	AI. 4 20 ms
				·		:	*
			Figure Ab	dh dh			

	Optional TDMA Slots			Optional TDMA Slots
	Sessions	Figure 5a		Sessions
501	Slot 1		203	RFP Addr 65
	Res. Poll			Res. Poll
	SYNC			SYNC

Figure 5b

	Optional TDMA Slots			Optional TDMA Slots			
	Sessions			Sessions			
109	Slot	Figure 6a		Slot			Figure 6b
		Fig.	4	RFF Addr 111	RFP Addr 02	(09	Fig
601	Slot 2			Slot 2			
601	Slot 1	603	1	RFP Addr 19			
	Res. Poll			Res. Poll			
	S X N C			O X X			

· j.

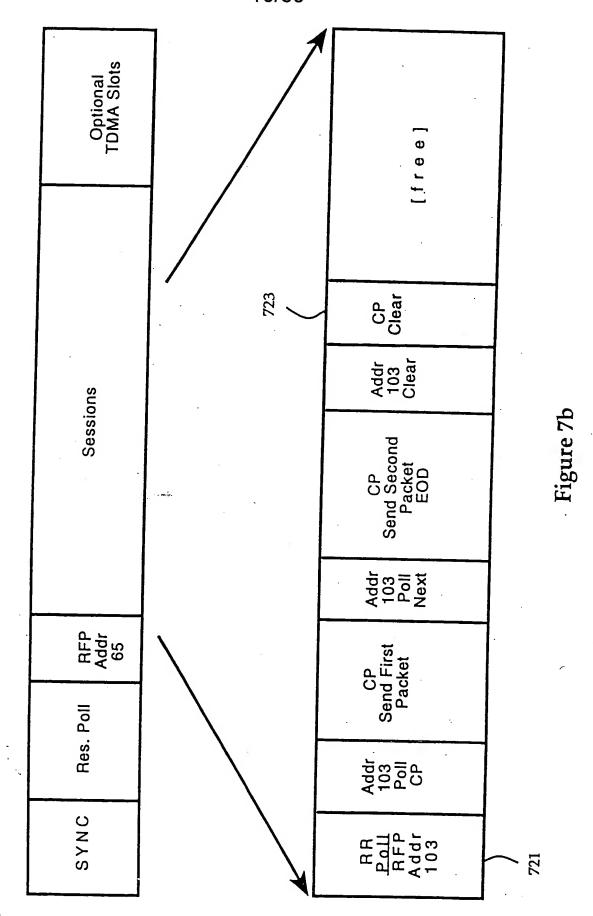
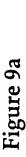
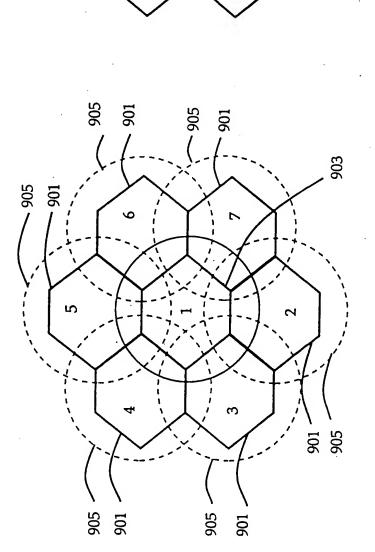
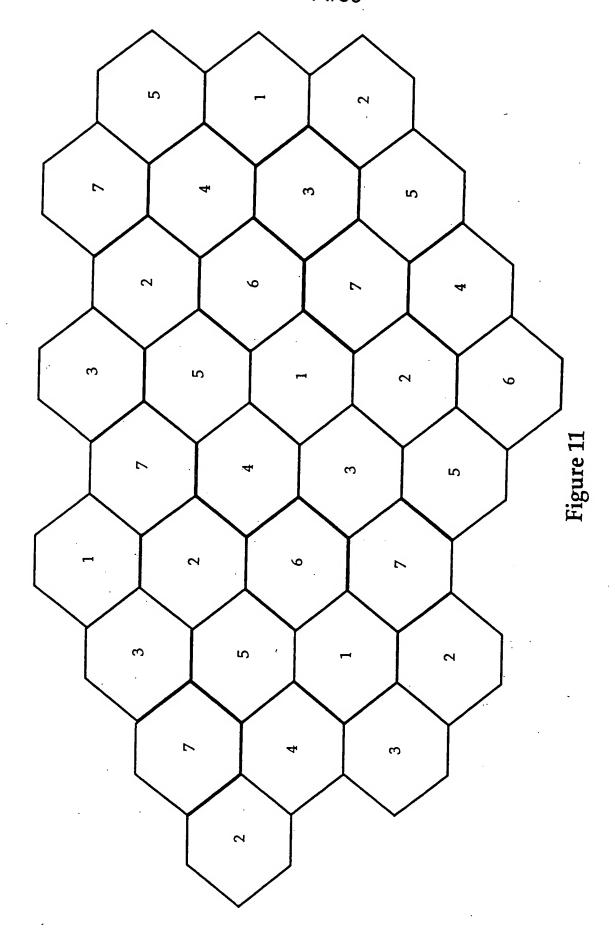


Figure 8









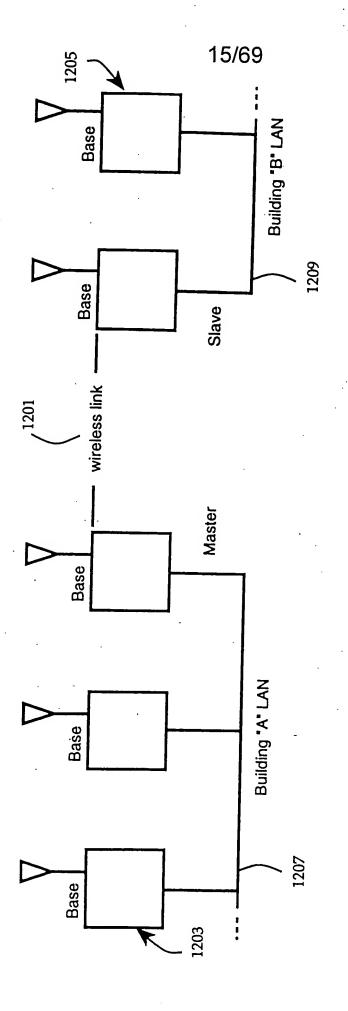


Figure 12

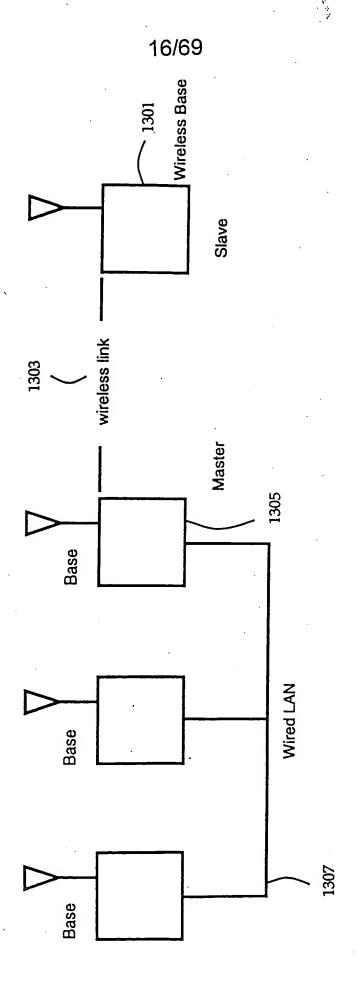


Figure 13

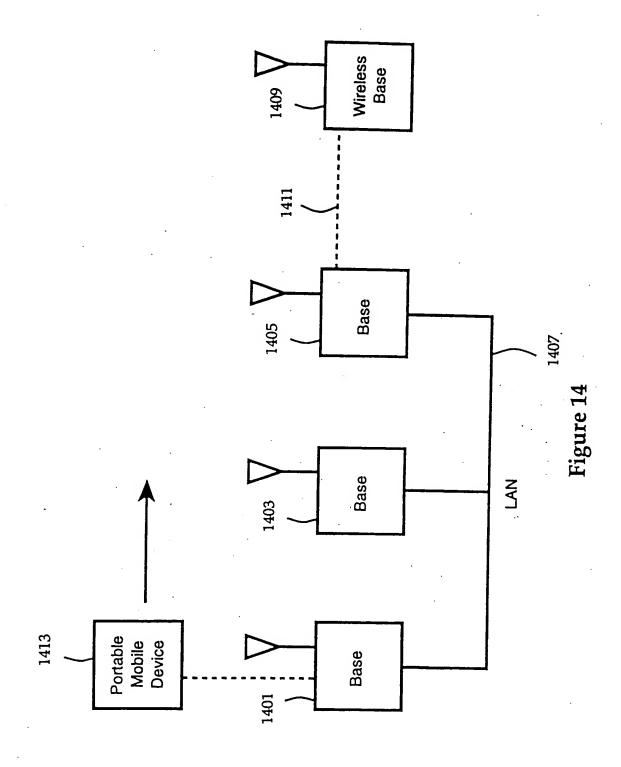


Figure 15

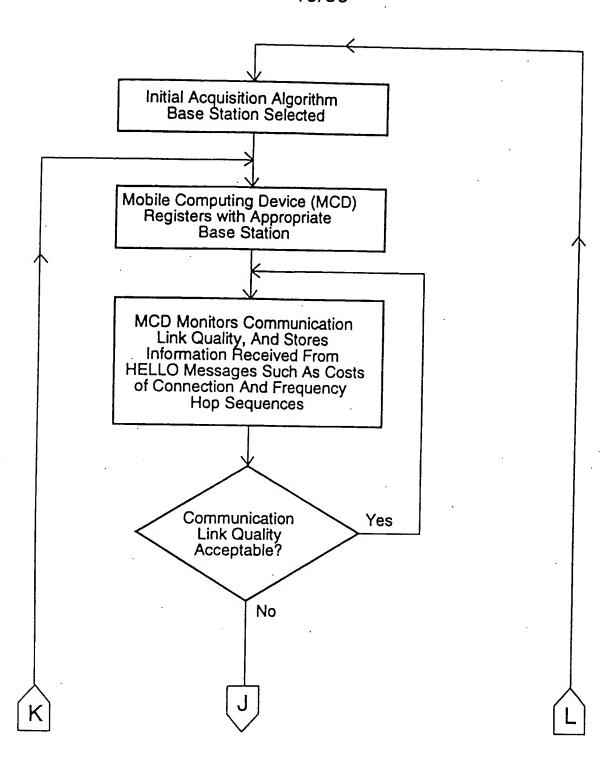
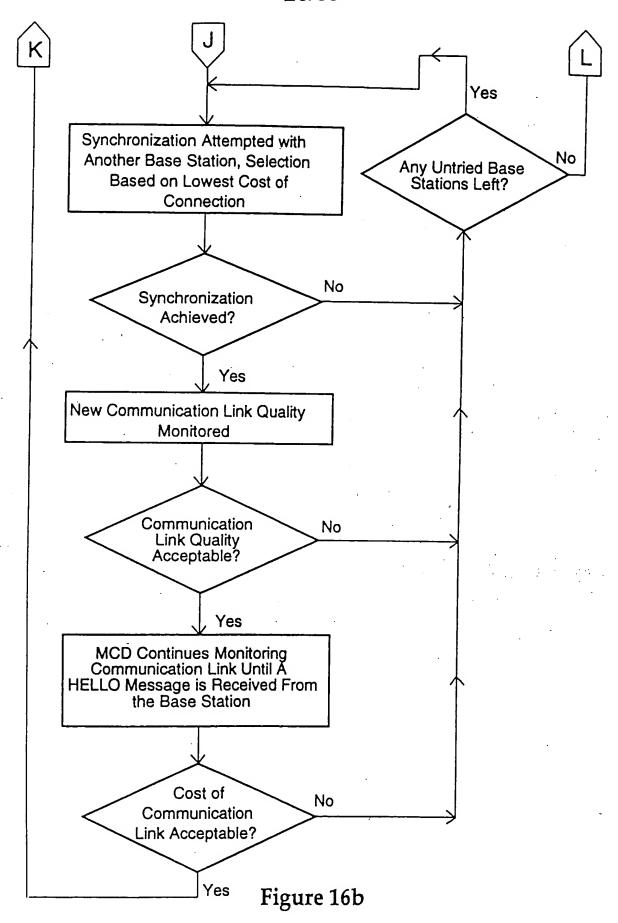


Figure 16a



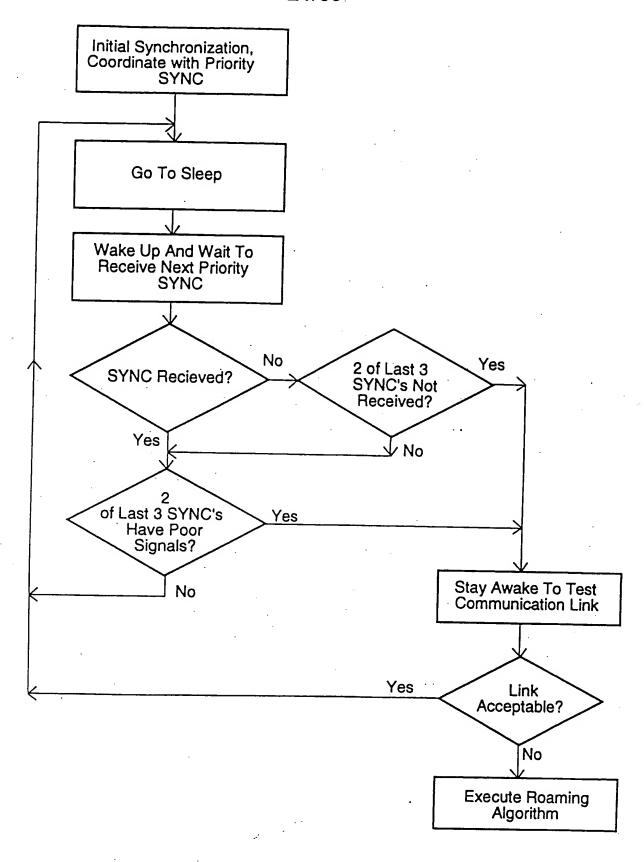
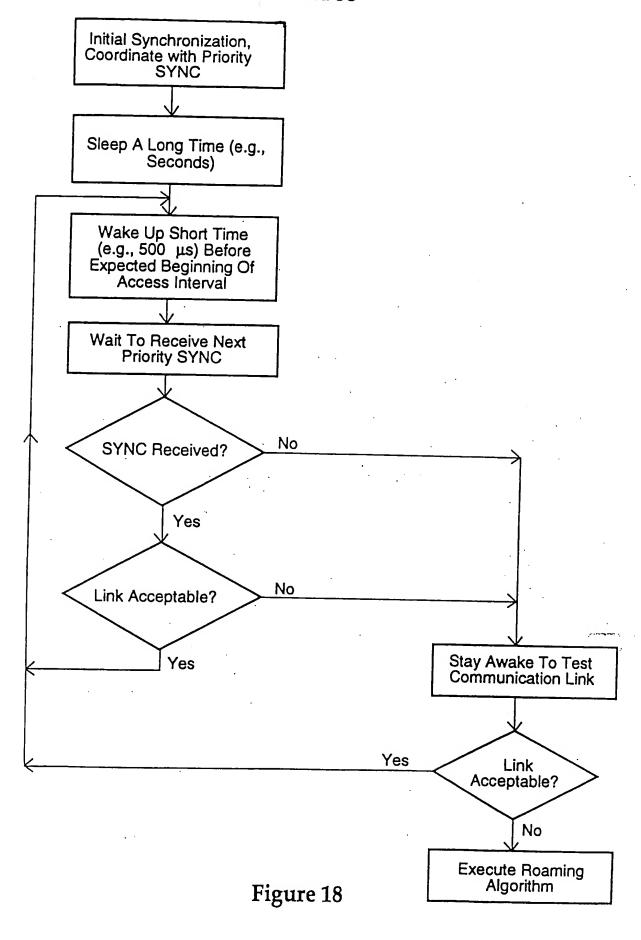
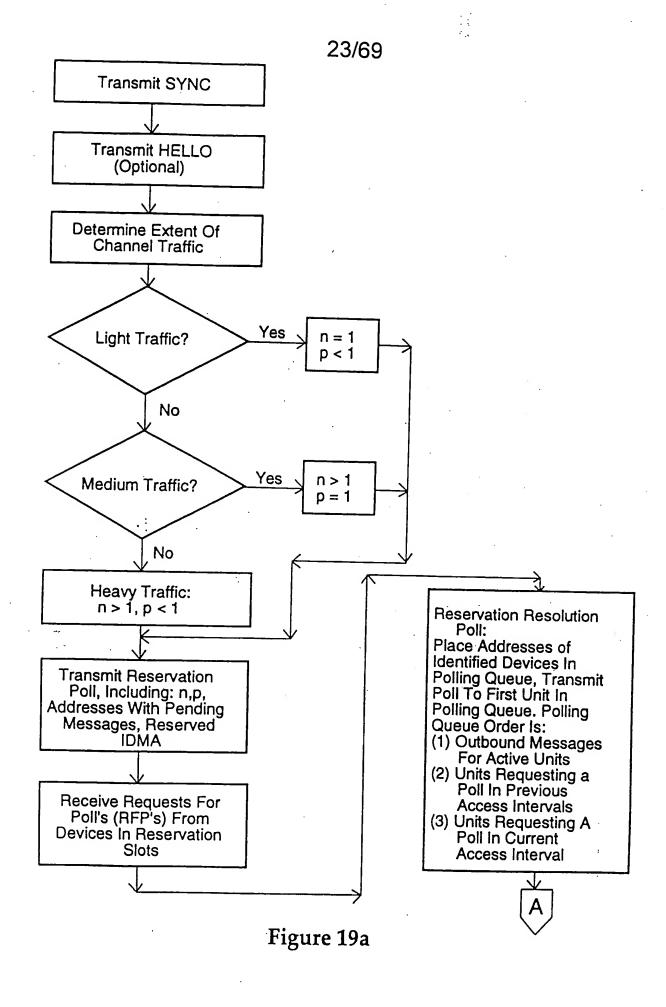


Figure 17





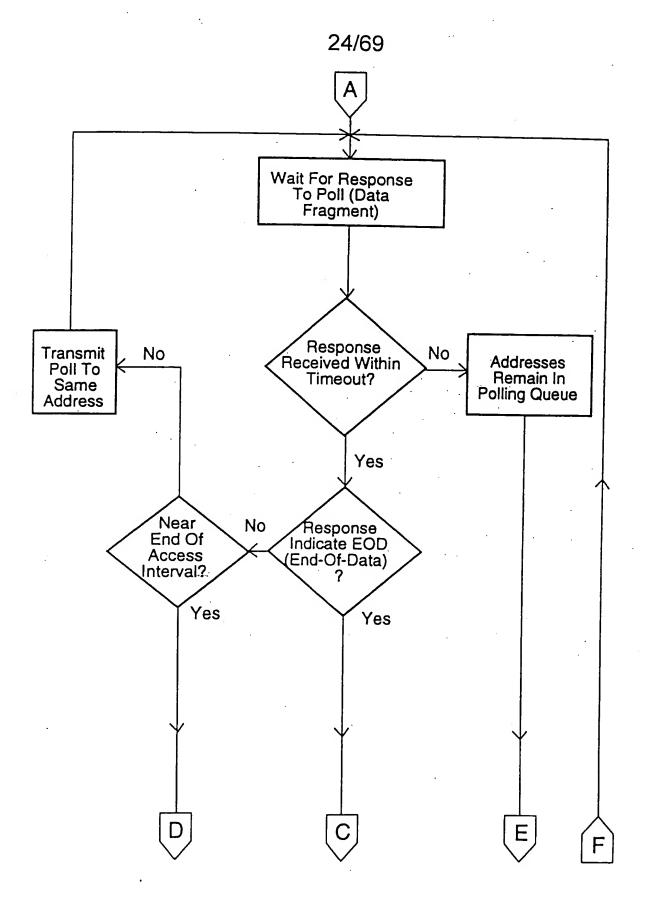


Figure 19b-1

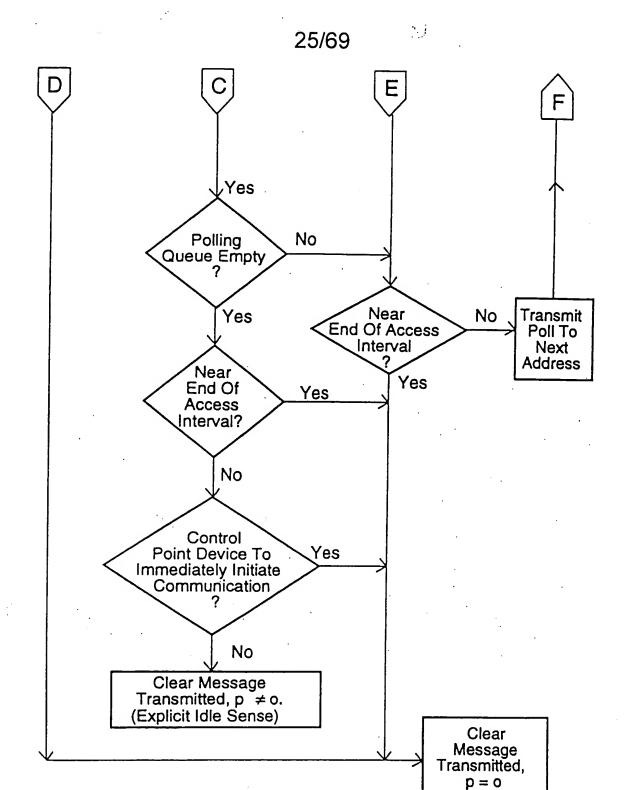
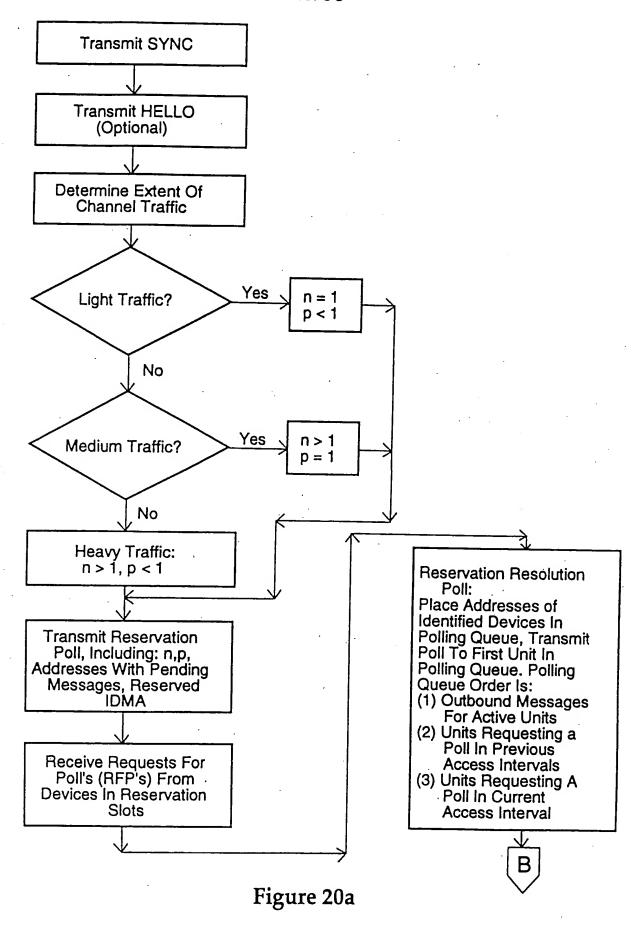


Figure 19b-2



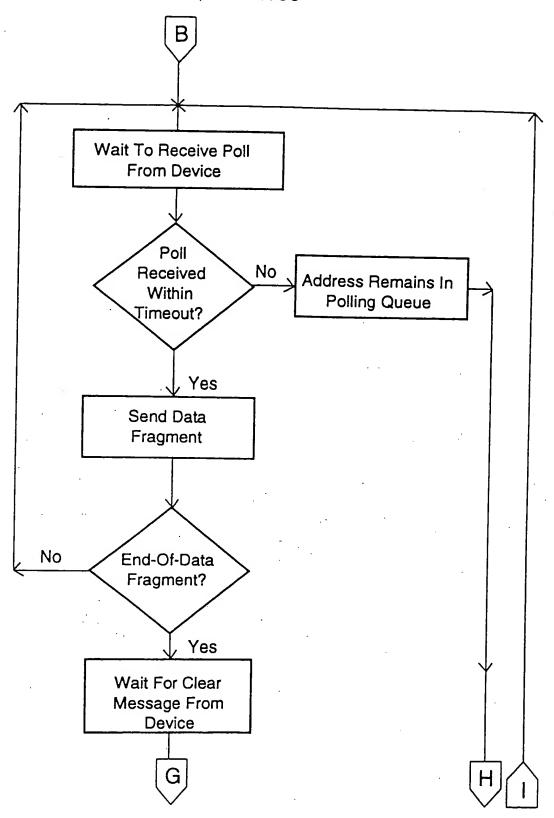


Figure 20b-1

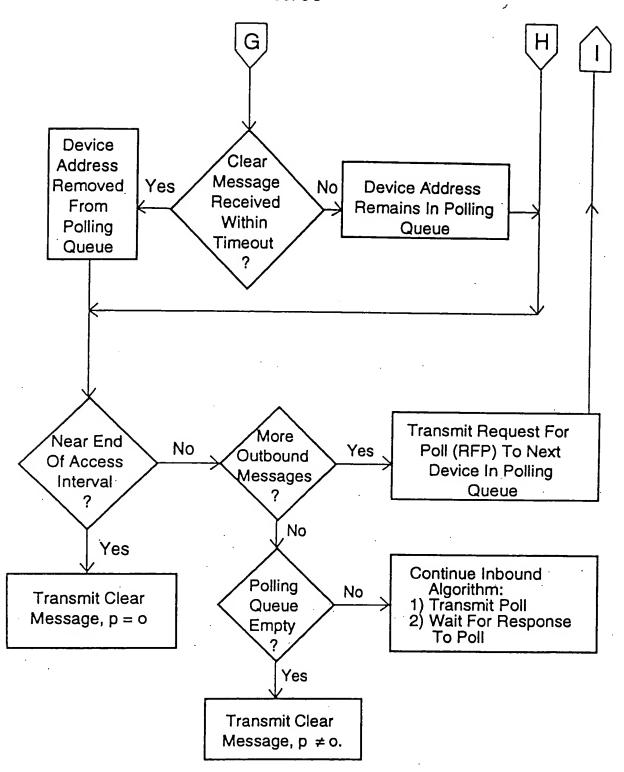


Figure 20b-2

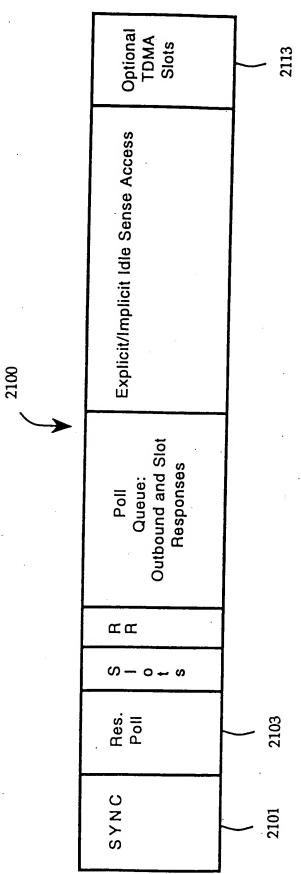


Figure 21

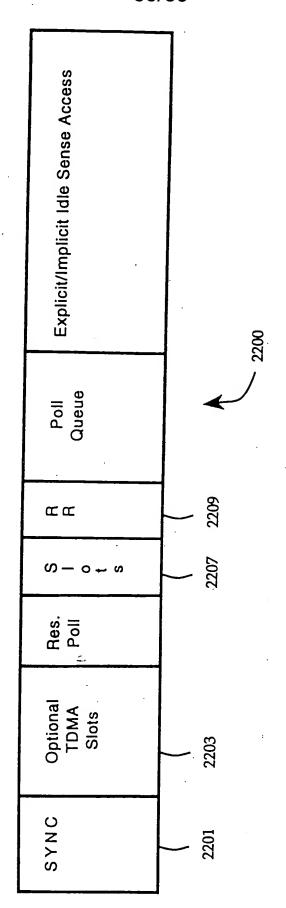


Figure 22

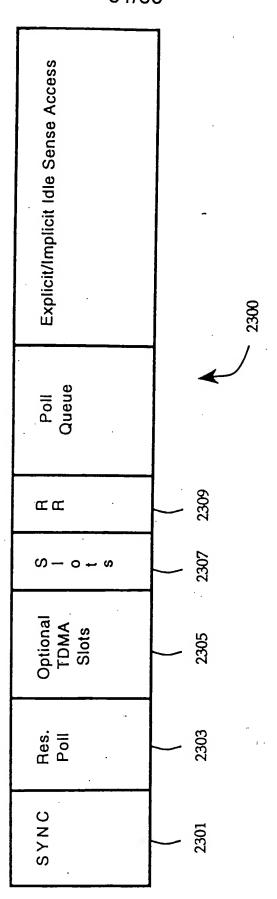
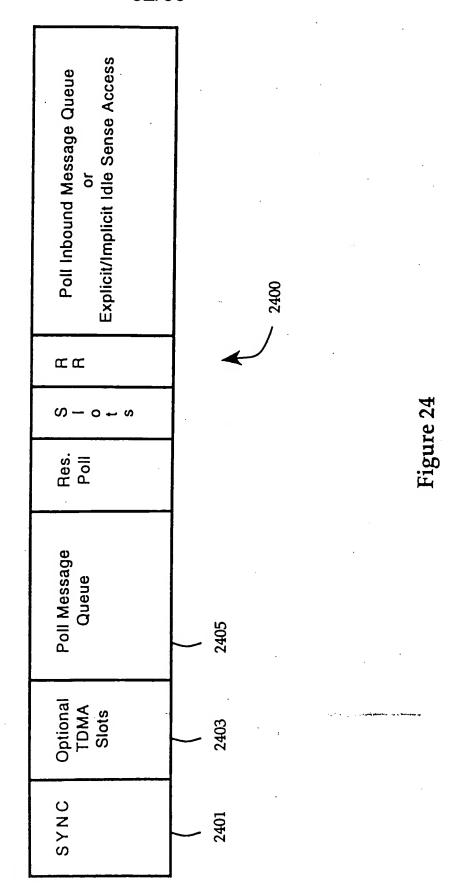
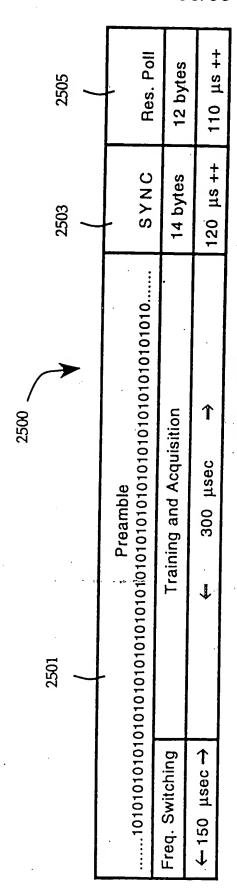


Figure 2.





++ allows additional bit times for HDLC zero bit insertion

Figure 25

		SYNC \$700
Byte	Content	Definition
	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _L)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with
		HELLO
2	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address. 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Net ID
* 6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10*	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
		3 Bits Number of Available Time Slots
11*	Random	Randomly Selected Character for Improved Engages
12	CRC 1	Fror Check
13	CRC 2	Fror Check
14	FLAG	Frame Delimiter

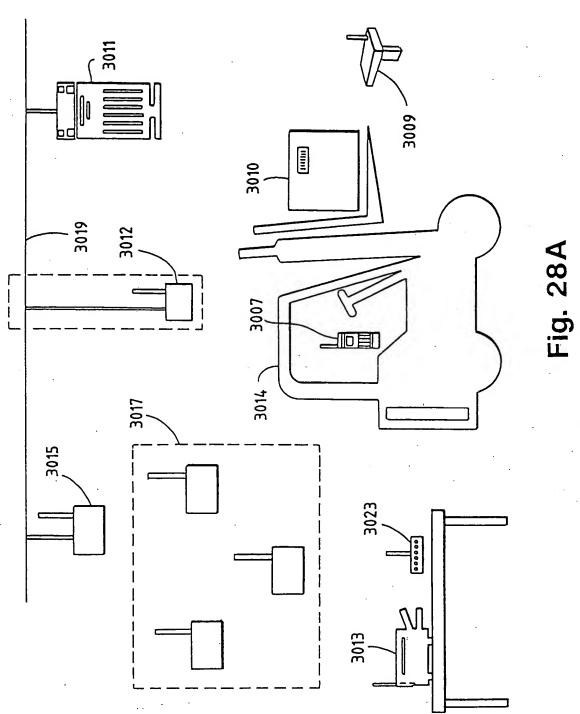
Figure 26

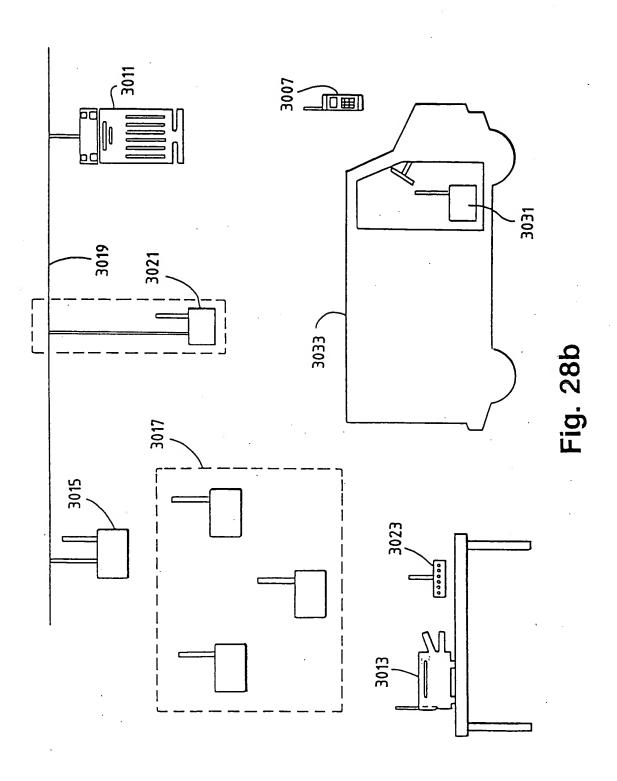
*Optionally Encrypted Bytes

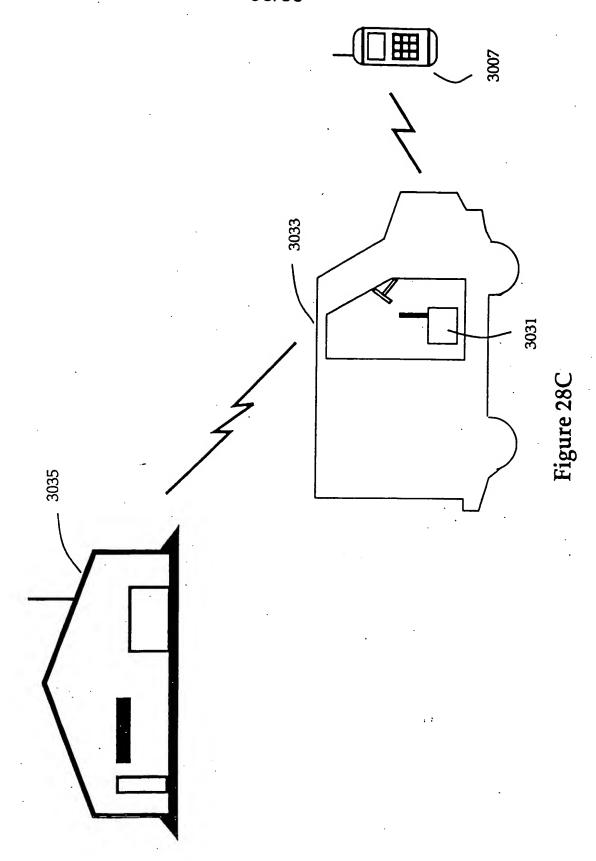
	Frame Delimiter Destination Address (RES = Broadcast = FFFF _H) Destination Address, 2nd Byte Message Type = RES POLL 3 Bits Number of Slots 5 Bits Probability Factor Hopping Sequence ID Hopping Sequence Index 2 Bits Al's per Hop 2 Bits Al's per Hop 2 Bits Al's Per Hop Berror Check	Dest. Address 1 Dest. Address 2 TYPE SLOT PARMS Index NET PARMS CRC 1	10 10 10 11
אסהס	Error Check	FLAG	12
		FLAG	7.5
	Error Check	- CHO	2 ;
225	,	CBC 1	10
CRC 1	3 Bits Reserved		
3 Bits Reserved CRC 1	2 Bits Al's per Hop		
2 Bits Al's per Hop 3 Bits Reserved CRC 1	3 Bits Hop Hate		
3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1			
3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1		NET PARMS	* 6
NET PARMS 3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1	Hopping Sequence Index	Index	8
Index NET PARMS 3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1	Hopping Sequence ID	Sequence	
Sequence Index NET PARMS 3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1	5 Bits Probability Factor		0
Sequence Index NET PARMS 3 Bits Al's per Hop 3 Bits Reserved CRC 1 3 Company Faster Served 3 CRC 1	3 Bits Number of Slots	SECT PARMS	
SECUL PAHMS 3 Bits Number of SI Sequence Index NET PARMS 3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1 3 Bits Reserved	Message 1 ype = RES PULL		-
SLOT PARMS 3 Bits Number of S Sequence Index NET PARMS 3 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1 3 Bits Reserved	Message Type DEC DO	TYPE	4
SLOT PARMS 3 Bits Number of S Sequence Index NET PARMS 3 Bits Probability F Sequence Index Sequence 2 Bits Hop Rate 2 Bits Al's per Hop 3 Bits Reserved CRC 1	Destination Address, 2nd Byte	Dest. Address 2	က
Dest. Address 2 TYPE SLOT PARMS 3 Bits Sequence Index NET PARMS 3 Bits SEQUENCE CRC 1 3 Bits	Destination Address (RES = Broadcast = FFFF _H)	Dest. Address 1	7
Dest. Address 1 Dest. Address 2 TYPE 3 Bits SLOT PARMS 3 Bits Sequence 5 Bits Index 3 Bits NET PARMS 2 Bits CRC 1 3 Bits	Frame Delimiter	באמ	-
Dest. Address 1 Dest. Address 2 TYPE SLOT PARMS 3 Bits Sequence Index NET PARMS 3 Bits Sequence CRC 1 3 Bits			
PLAG PLAG Dest. Address 1 Dest. Address 2 TYPE SLOT PARMS 3 Bits Sequence Index NET PARMS 3 Bits Sequence CRC 1 3 Bits	Definition		21.5

Reservation Poll

Figure 27







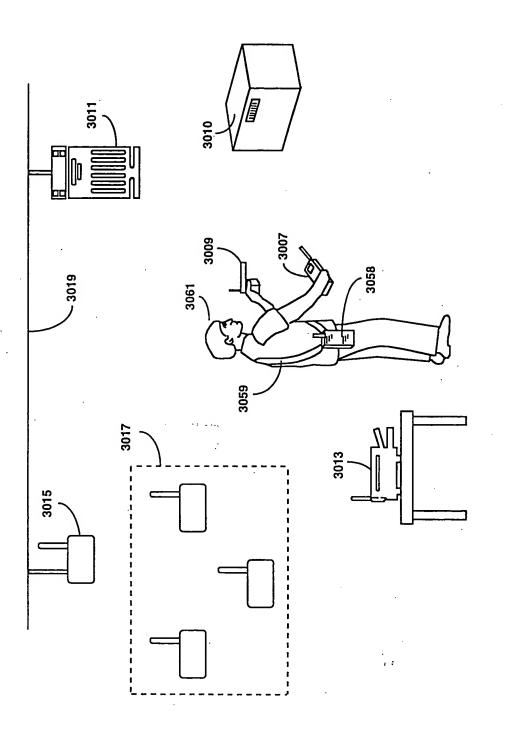


Fig. 298

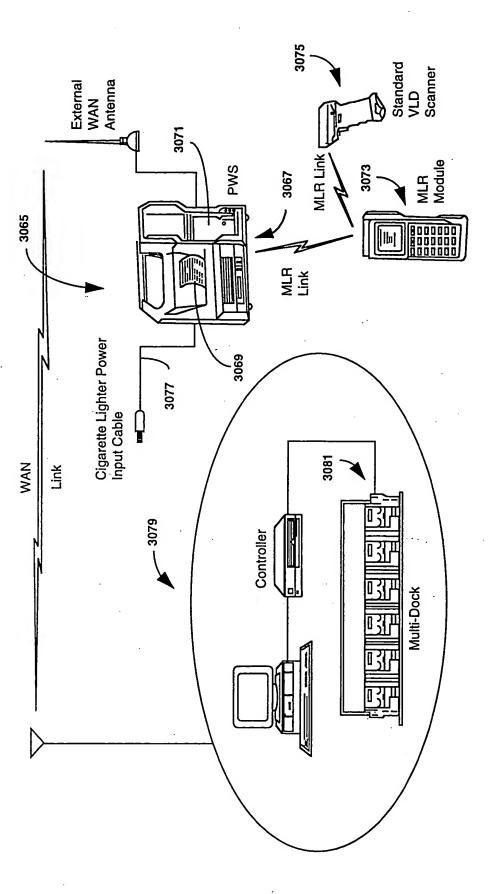
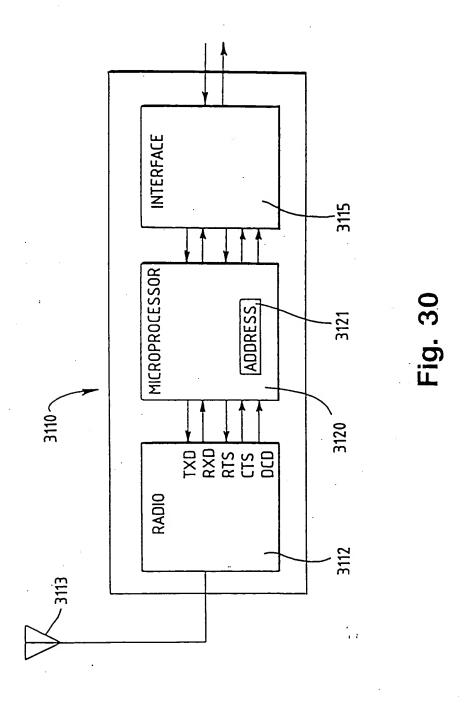


Fig. 29b



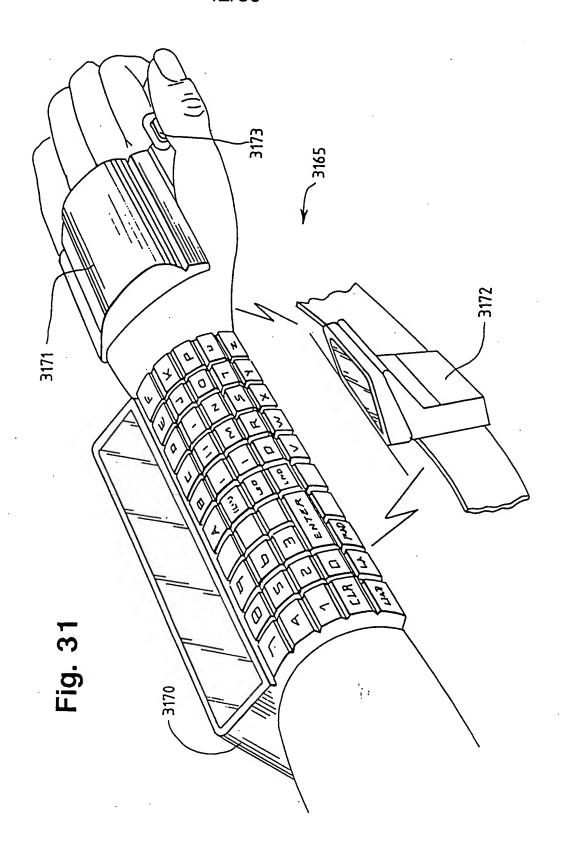
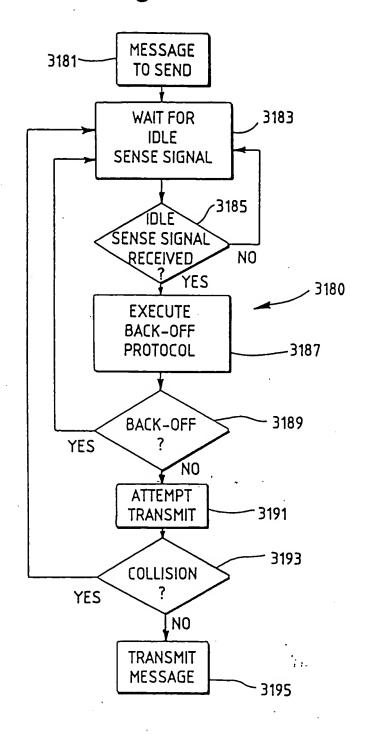


Fig. 32



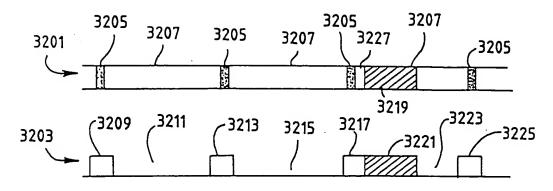
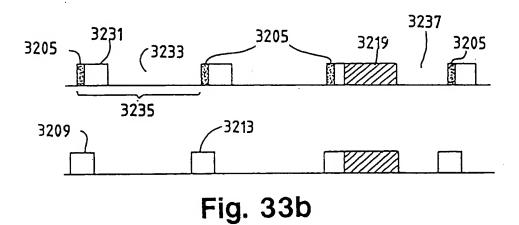
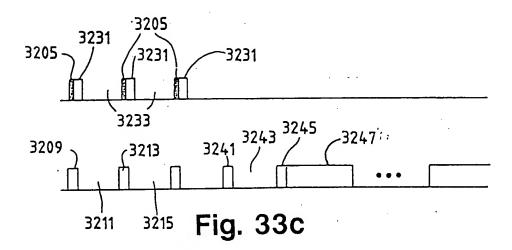


Fig. 33a





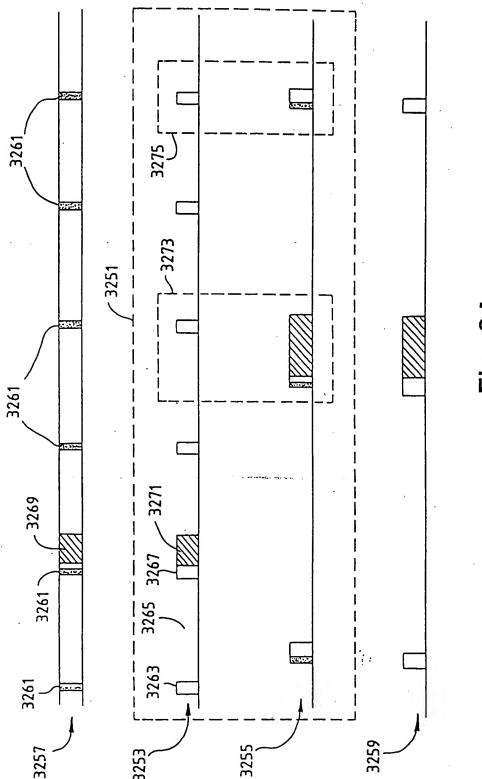


Fig. 34

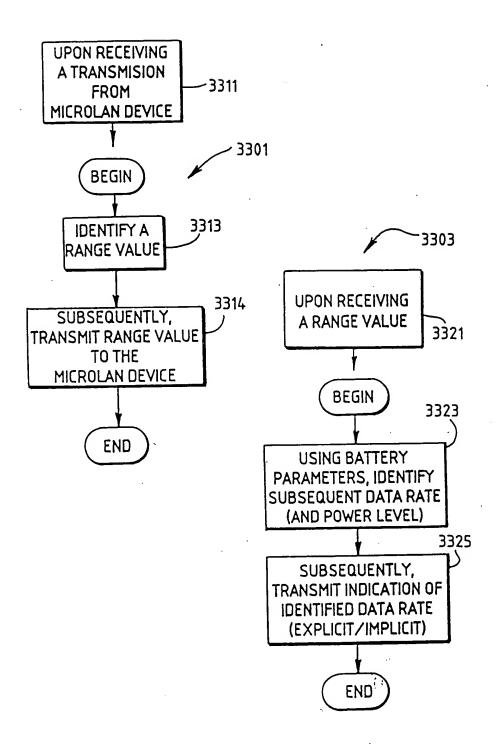
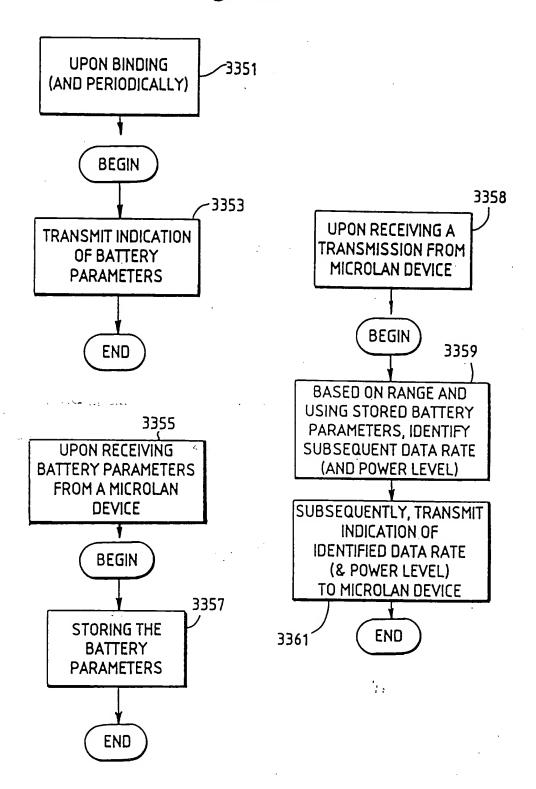


Fig. 35

Fig. 36



:

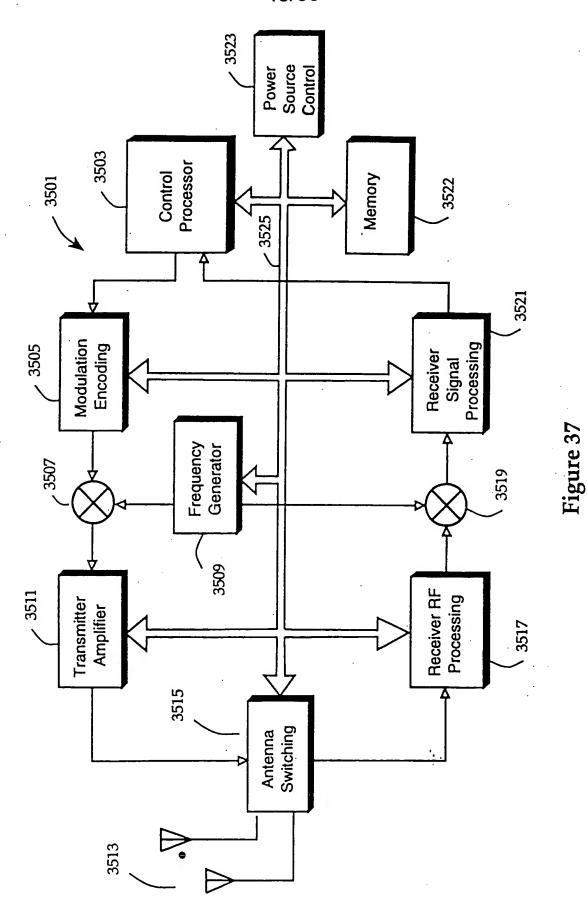


Figure 38

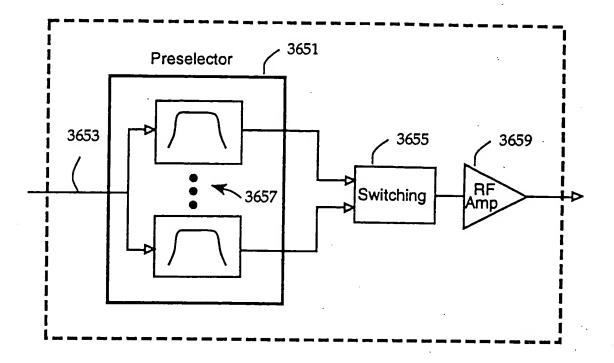


Figure 39

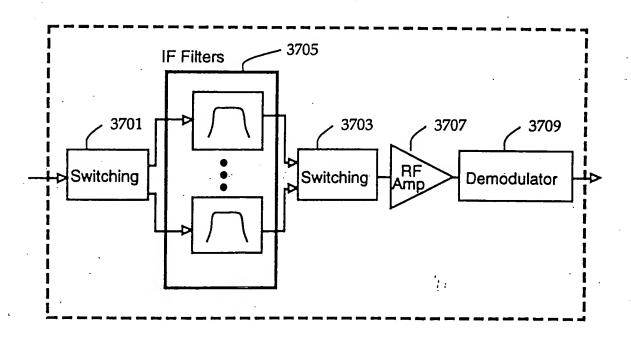
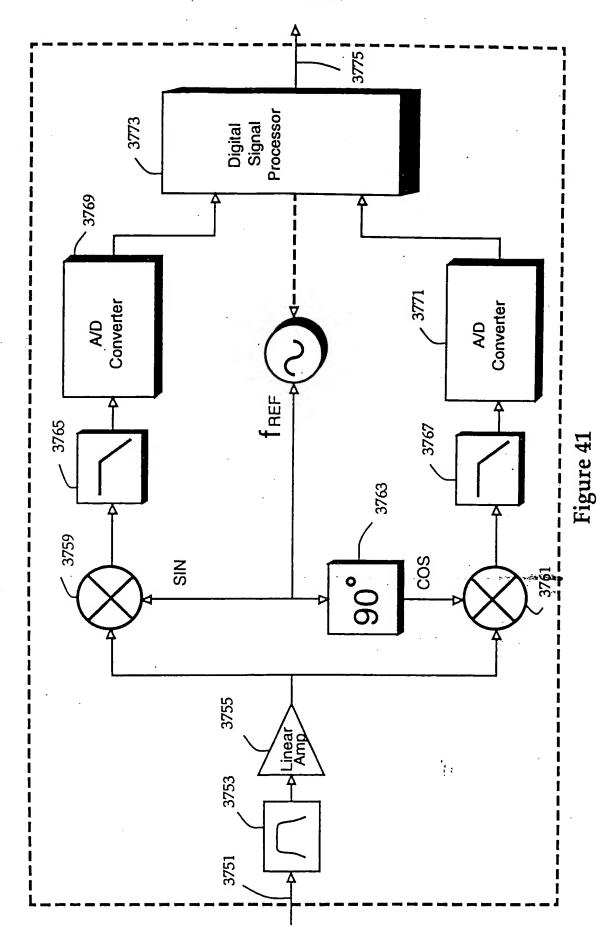


Figure 40



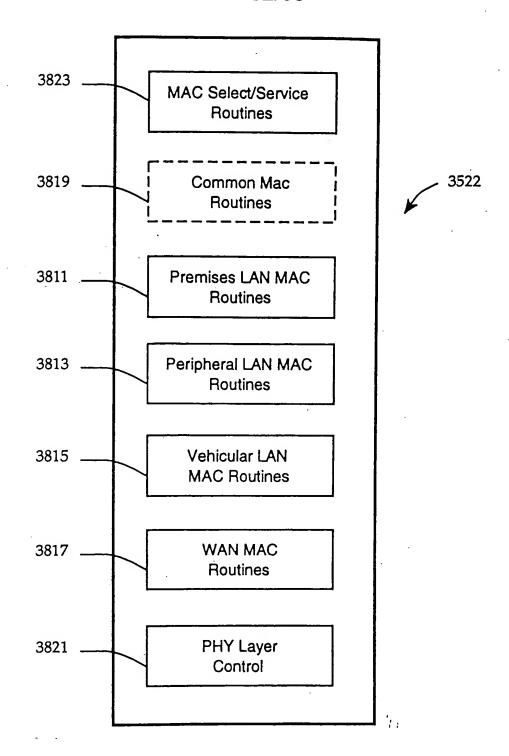
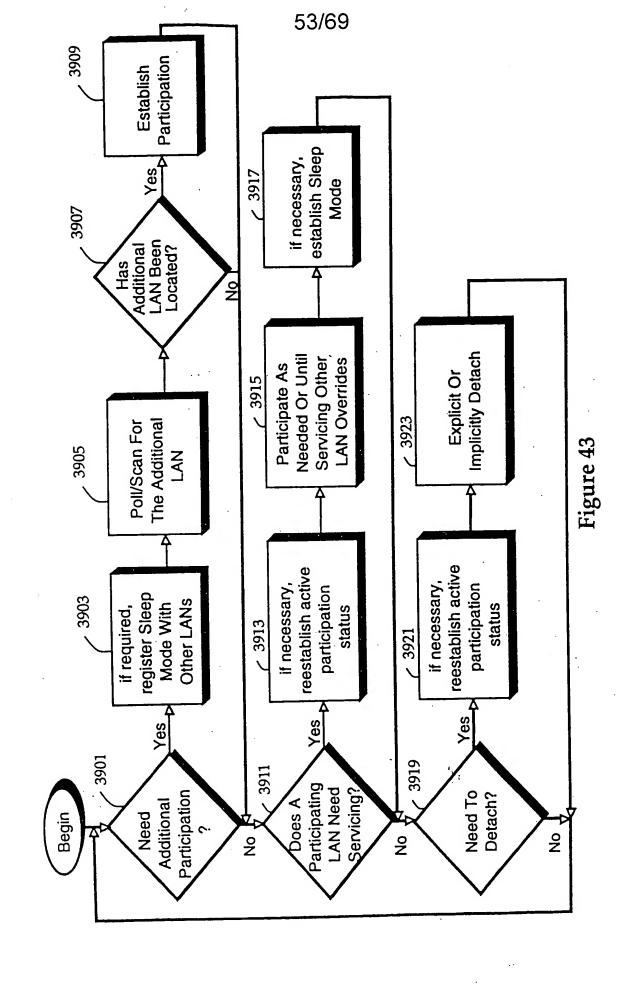


Figure 42



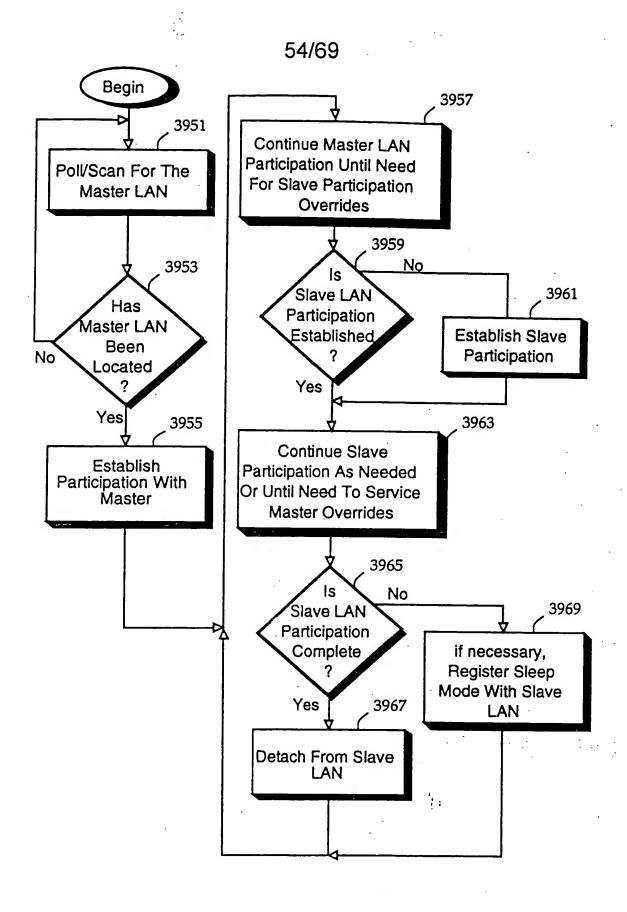
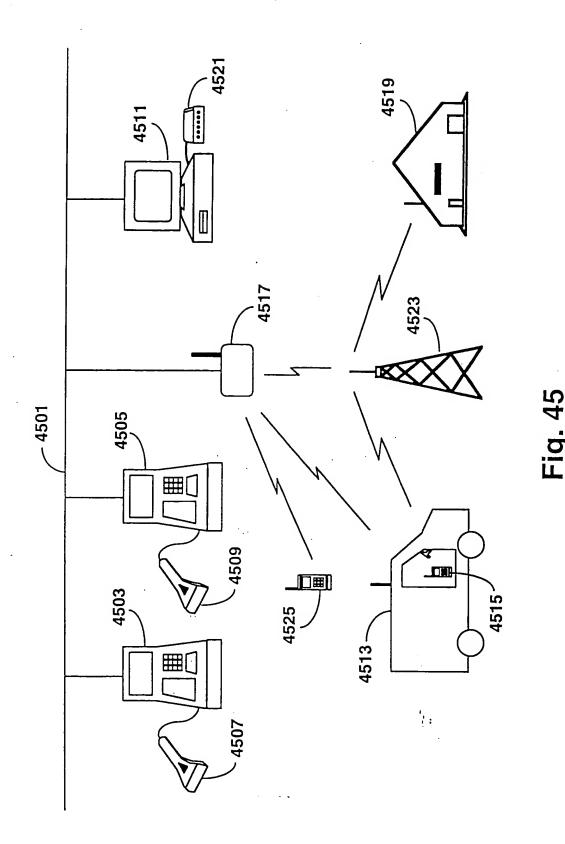


Figure 44



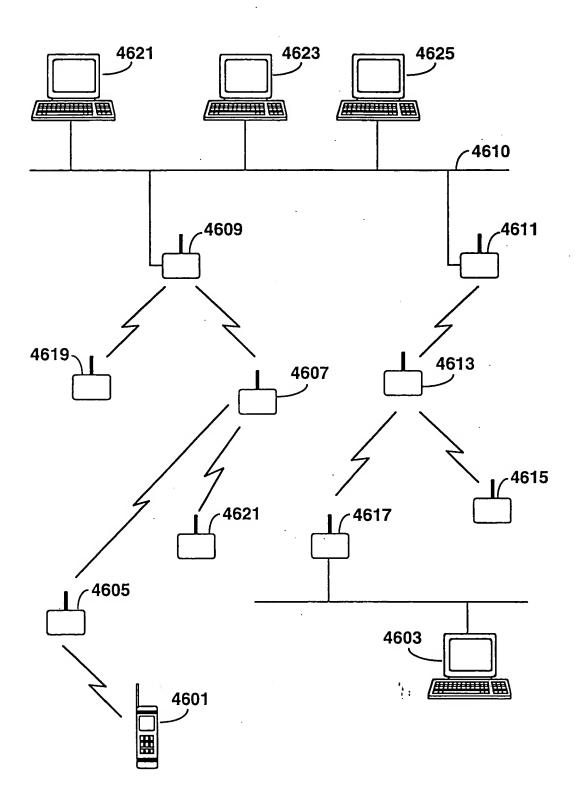


Fig. 46a

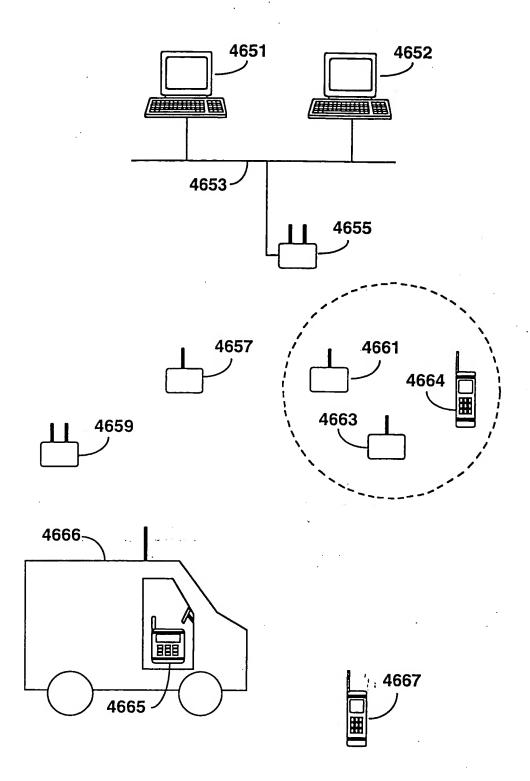


Fig. 46b

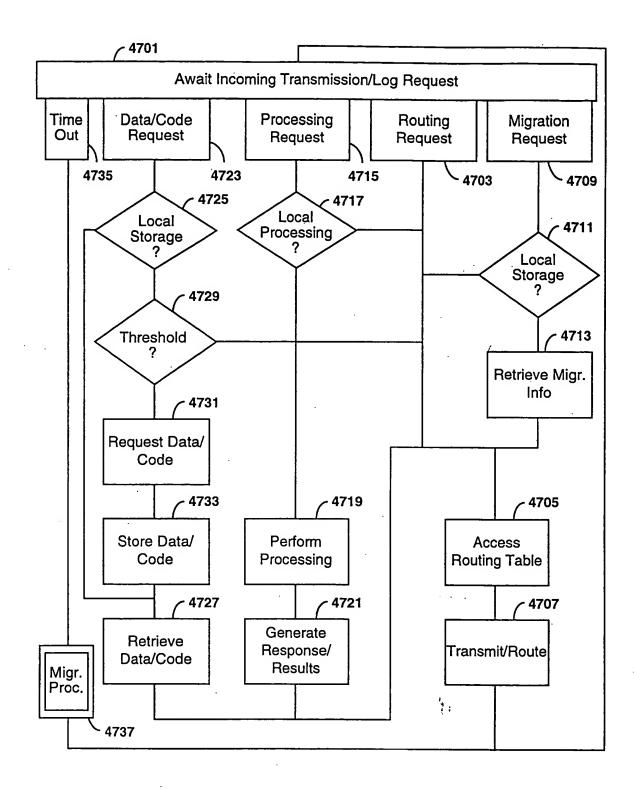


Fig. 47a

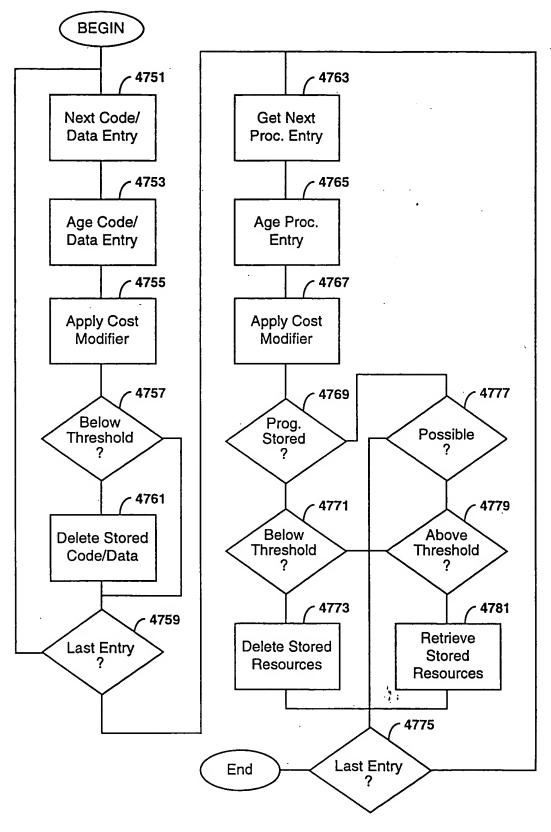


Fig. 47b

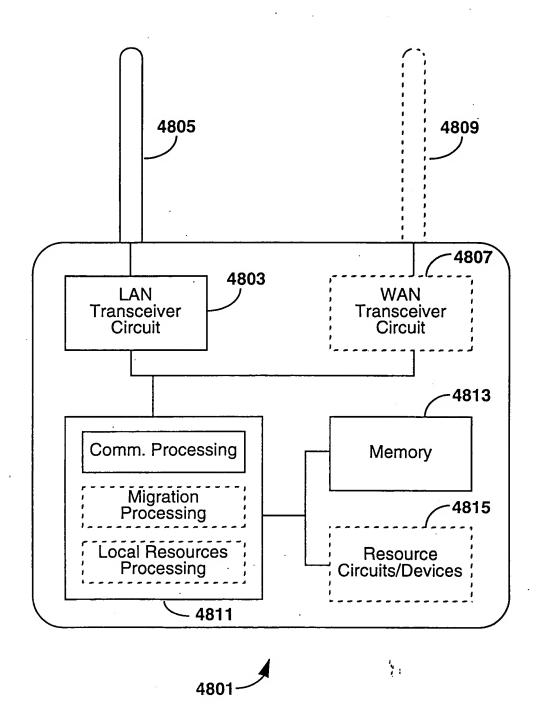


Fig. 48

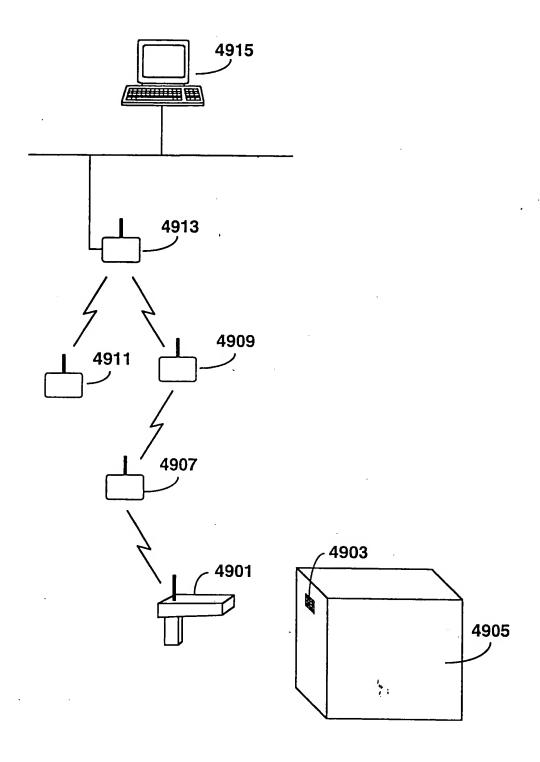


Fig. 49a

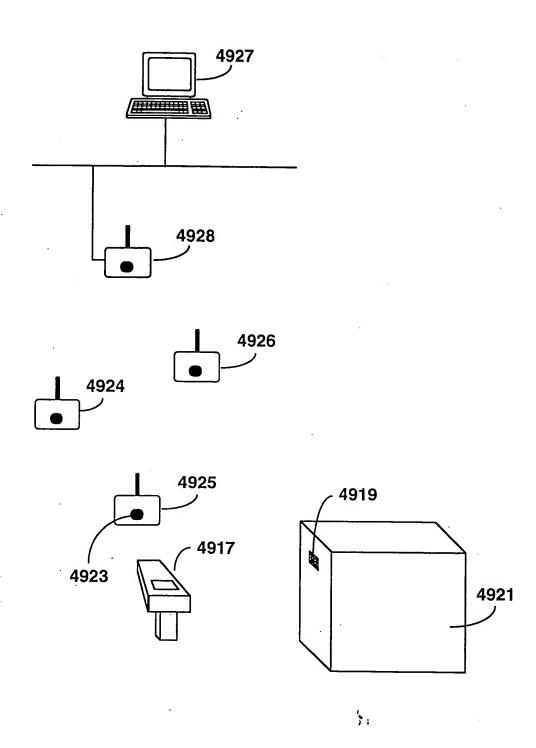


Fig. 49b

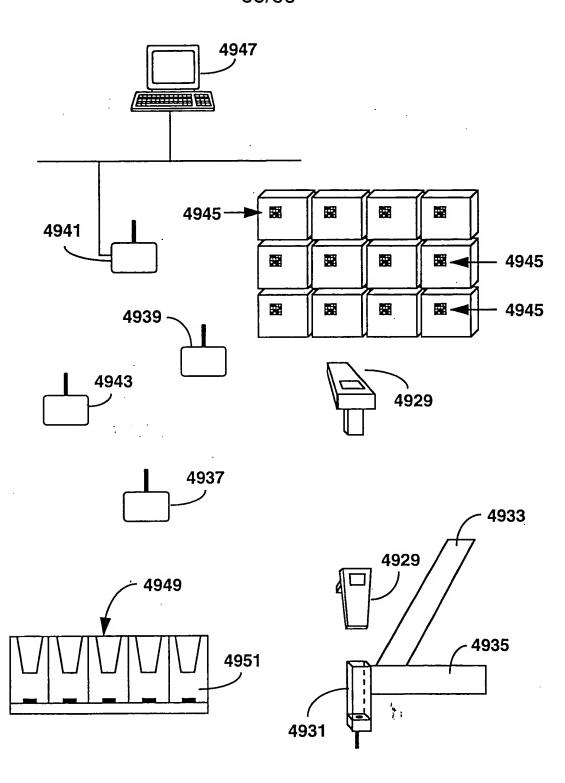


Fig. 49c

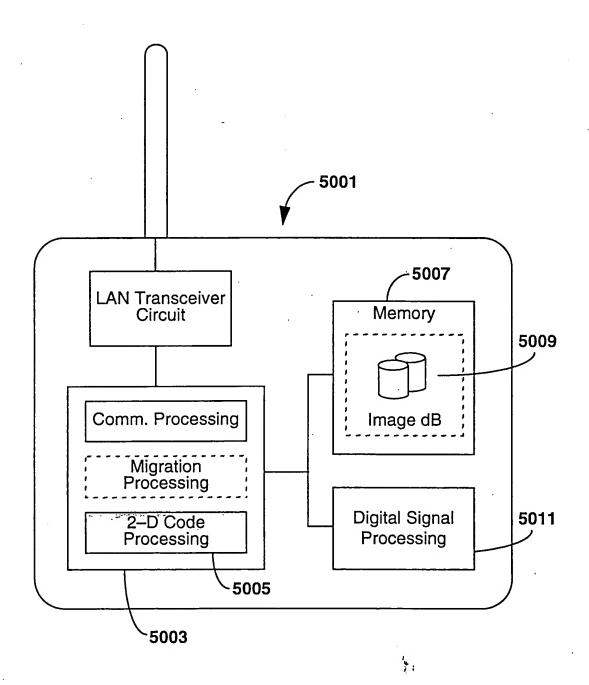


Fig. 50

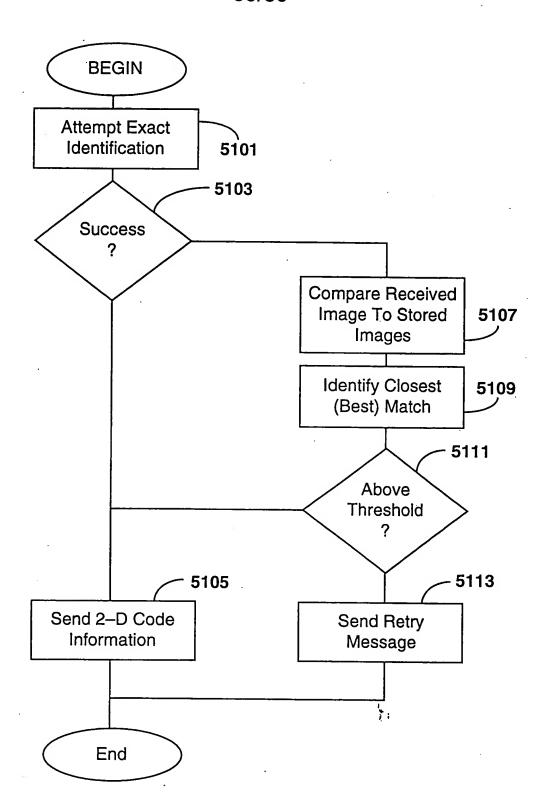


Fig. 51a

÷.

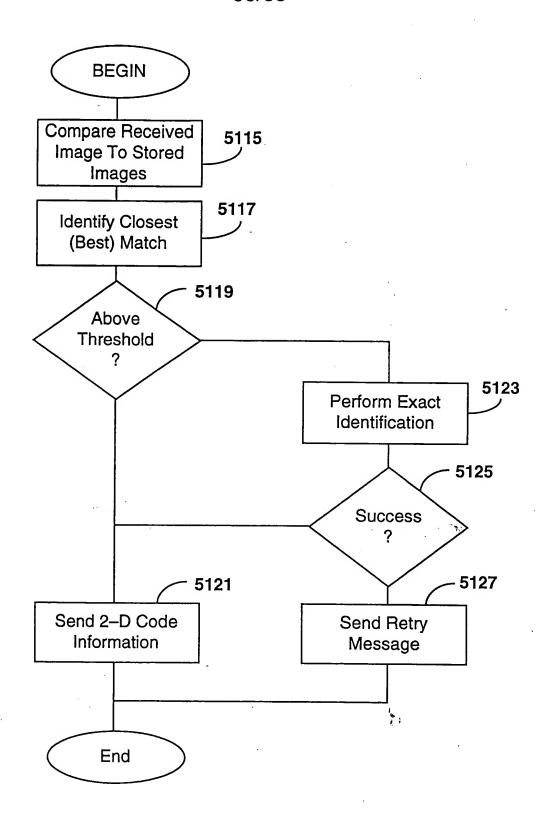
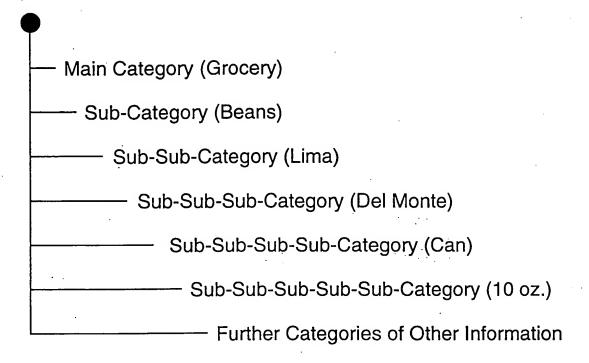


Fig. 51b



· ;

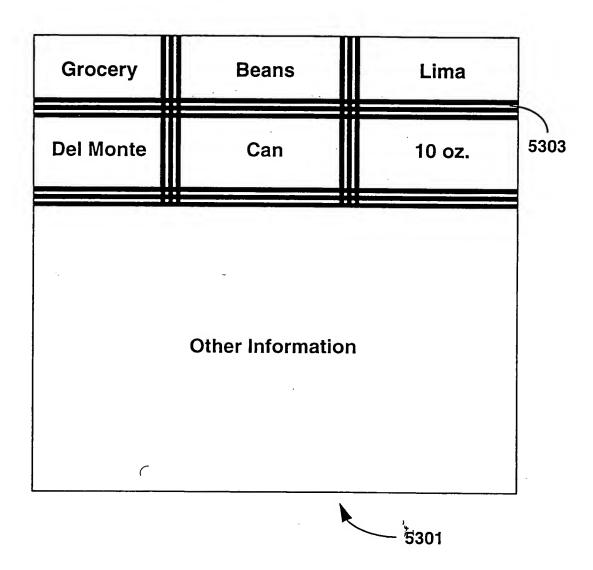


Fig. 53

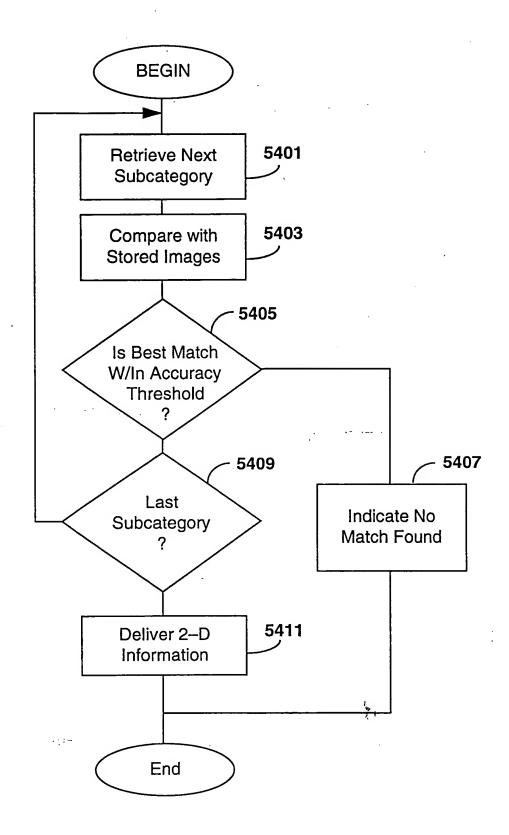


Fig. 54